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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Nathan Y. Moyal

Serial No.: 09/966,481

Filed: September 28, 2001

For: Generating Pulses For Resetting
Integrated Circuits

§ Group Art Unit: 2819

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§ Examiner:

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§ Atty. Dkt. No.: INTL-0552-US

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Commissioner for Patents
Washington DC 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of which
are enclosed.

This statement is being filed within three months of the filing date of the application.

Please apply any charges or credits to Deposit Account No. 20-1504 (INTL-0552-US).

Respectfully submitted,

Date: 11/26/01

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Date of Deposit: November 26, 2001

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, Washington DC 20231.

Sherry Tipton